

REMARKS

Reconsideration and allowance of this application are respectfully requested in view of the above and the discussion below.

Applicants invention, as defined by independent claims 5 and 9, concerns an improved data bus which allows for interference-free bus communication with low circuit-technological expenditure even when a large number of bus nodes are used. A logical decision gate 1 has inputs corresponding to signal outputs of the bus nodes. The logical decision gate transmits the signals at low power consumption and may be used with nodes which supply electrical output information as well as with nodes which generate optical output signals.

Data exchange modules are connected to the nodes T_n and provide a corresponding electrical output signal. The output of the logical decision gate is a single output, which is fed to input of each one of the data exchange modules S/E.

Claims 5-6 and 9 have been rejected under 35 U.S.C. 102 as anticipated by Tominaga et al. as indicated at item 6 at the bottom of page 3 and the top of page 4 of the Office Action. According to the statement of this rejection, reference is made to Figure 1a of Tominaga et al. for the disclosure of the subject matter of claims 5-6 and 9.

Allowable subject matter has been indicated at claims 7-8 and 10.

Applicants respectfully traverse the rejection of claims 5-6 and 10 over the reference to Tominaga et al. on the grounds that Figure 1a of Tominaga et al. is a

logical configuration which does not reflect the actual construction. The system of the Tominaga et al. reference does not have any actual construction containing logical gates as required by the claimed invention. The system functions as a “wired OR” system which is an energy consuming system improved upon by the present invention.

The actual construction of Tominaga et al. is shown in Figure 1b as an internal bus. Applicants presently claimed invention defined by independent claims 5 and 9 therefore provides subject matter not shown by the reference.

In response to the objection to the specification and claims 5-8, the above amendments provide the appropriate correction without adding any new matter.

In response to the objection to the drawings, Applicants are submitting herewith a proposed drawing correction to be implemented upon approval of the Examiner.

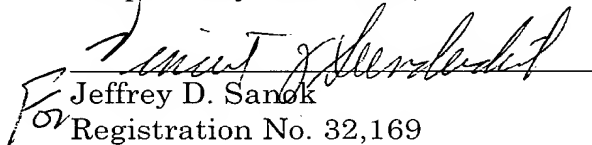
Therefore in view of the distinguishing features between the claimed invention and the references, Applicants respectfully request that this application containing claims 5-10 be allowed and be passed to issue.

If there are any questions regarding this amendment or the application in general, a telephone call to the undersigned would be appreciated since this should expedite the prosecution of the application for all concerned.

If necessary to effect a timely response, this paper should be considered as a petition for an Extension of Time sufficient to effect a timely response, and please charge any deficiency in fees or credit any overpayments to Deposit Account No. 05-1323 (Docket #080437.49166US).

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Respectfully submitted,


For Jeffrey D. Sano
Registration No. 32,169

CROWELL & MORING, LLP
P.O. Box 14300
Washington, DC 20044-4300
Telephone No.: (202) 624-2500
Facsimile No.: (202) 628-8844

JDS:adb